

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (Currently Amended): A current feedback-type operational amplifier comprising multiple input parts and one output part, wherein each of said multiple input parts comprises a first input terminal, a second input terminal, and first output terminal, ~~[[the]]~~wherein signals input from said first input terminal are buffer amplified and output to said second input terminal, and wherein current is output to the first output terminal in an amount corresponding to~~[[the]]~~ current that flows to said second input terminal;

said ~~output terminal~~ part comprises a third input terminal and a second output terminal, wherein signals obtained by adding in terms of current the signals of all of said multiple input parts are input to said third input terminal, and ~~[[the]]~~wherein signals input to said third input terminal are converted to voltage signals, amplified, and output to the second output terminal as an output signal; and

one of said multiple input parts is ~~made effective~~enabled and the other of said multiple input parts are ~~made ineffective~~disabled in response to a first external signal, wherein the impedance of said first input terminal, said second input terminal, and said first output terminal of said ~~ineffective~~disabled input parts becomes high and the output current from said first output terminal becomes zero, and only the signals input to said ~~effective~~enabled input parts are thereby amplified.

Claim 2 (Currently Amended): The current feedback-type operational amplifier as described in claim 1, wherein all of said multiple input parts and said output part are ~~made ineffective~~disabled in response to second external signals; this results in the impedance of said first input terminal and said second input terminal of said

~~ineffective~~disabled input parts becoming high and ~~[[the]]~~ output current from said output terminal becoming zero; and the impedance of said output terminal of said output part becomes high.

Claim 3 (Original): The current feedback-type operational amplifier according to claim 1, wherein each of said input parts outputs current to said first output terminal in the same amount as the current flowing to said second input terminal.

Claim 4 (Original): The current feedback-type operational amplifier according to claim 1, wherein each of said input parts comprises a voltage buffer that regards said first input terminal as its input terminal and said second input terminal as its output terminal, and a current mirror circuit connected to said voltage buffer.

Claim 5 (Currently Amended): A signal-switching amplifying apparatus comprising a current feedback-type operational amplifier comprising multiple input parts and one output part, wherein each of said multiple input parts comprises a first input terminal, a second input terminal, and first output terminal, ~~[[the]]~~wherein signals input from said first input terminal are buffer amplified and output to said second input terminal, and wherein current is output to the first output terminal in an amount corresponding to ~~[[the]]~~ current that flows to said second input terminal;

said output ~~terminal~~ part comprises a third input terminal and a second output terminal, wherein signals obtained by adding in terms of current the signals of all of said multiple input parts are input to said third input terminal, and ~~[[the]]~~wherein signals input to said third input terminal are converted to voltage signals, amplified, and output to the second output terminal as an output signal; and

one of said multiple input parts is ~~made-effective~~enabled and the other of said multiple input parts are ~~made-ineffective~~disabled in response to a first external signal, wherein the impedance of said first input terminal, said second input terminal, and said first output terminal of said ~~ineffective~~disabled input parts becomes high and the output current from said first output terminal becomes zero, and only the signals input to said ~~effective~~enabled input parts are thereby amplified.

Claim 6 (Currently Amended): A variable-gain amplifying apparatus which comprises a current feedback-type operational amplifier comprising multiple input parts and one output part, wherein each of said multiple input parts comprises a first input terminal, a second input terminal, and first output terminal, ~~[[the]]~~wherein signals input from said first input terminal are buffer amplified and output to said second input terminal, and wherein current is output to the first output terminal in an amount corresponding to~~[[the]]~~ current that flows to said second input terminal;

said ~~output-terminal~~ part comprises a third input terminal and a second output terminal, wherein signals obtained by adding in terms of current the signals of all of said multiple input parts are input to said third input terminal, and ~~[[the]]~~wherein signals input to said third input terminal are converted to voltage signals, amplified, and output to the second output terminal as an output signal; and

one of said multiple input parts is ~~made-effective~~enabled and the other of said multiple input parts are ~~made-ineffective~~disabled in response to a first external signal, wherein the impedance of said first input terminal, said second input terminal, and said first output terminal of said ~~ineffective~~disabled input parts becomes high and the output current from said first output terminal becomes zero, and only the signals input to said ~~effective~~enabled input parts are thereby amplified.

Claim 7 (Currently Amended): A variable-band amplifying apparatus which comprises a current feedback-type operational amplifier comprising multiple input parts and one output part, wherein each of said multiple input parts comprises a first input terminal, a second input terminal, and first output terminal, ~~[[the]]~~wherein signals input from said first input terminal are buffer amplified and output to said second input terminal, and wherein current is output to the first output terminal in an amount corresponding to~~[[the]]~~ current that flows to said second input terminal;

said ~~output-terminal~~ part comprises a third input terminal and a second output terminal, wherein signals obtained by adding in terms of current the signals of all of said multiple input parts are input to said third input terminal, and ~~[[the]]~~wherein

signals input to said third input terminal are converted to voltage signals, amplified, and output to the second output terminal as an output signal; and

one of said multiple input parts is ~~made ineffective~~enabled and the other of said multiple input parts are ~~made ineffective~~disabled in response to a first external signal, wherein the impedance of said first input terminal, said second input terminal, and said first output terminal of said ~~ineffective~~disabled input parts becomes high and the output current from said first output terminal becomes zero, and only the signals input to said ~~effective~~enabled input parts are thereby amplified.